Example Concurrent Program

int x = 0CO x = x + 1x = x + 2**OC** print x

What are the possible outputs of this program?

1

Example Concurrent Program (cont.)

- One possible execution order is:
 - Thread 0: R1 := x (R1 == 0)
 - Thread 1: R2 := x (R2 == 0)
 - Thread 1: R2 := R2 + 2 (R2 == 2)
 - Thread 1: x := R2 (x == 2)
 - Thread 0: R1 := R1 + 1 (R1 == 1)
 - Thread 0: x := R1 (x == 1)
- Final value of x is 1 (!!)
- Question: what if Thread 1 also uses R1? ²

Example Concurrent Program

int x = 0CO x = x + 1x = x + 2**OC** print x

Possible outputs are 1, 2, and 3 The output **cannot** be 0 because of the oc More Concurrent Programming: Linked Lists (head is shared)

Insert(head, elem) { elem→next := head; head := elem;

Void *Remove(head) {
Void *t;
t:= head;
head := head→next;
return t;
}

(Assume one thread calls Insert and one calls Remove, concurrently)

One Possible (Fine) Execution



Remove: t := head;



One Possible (Bad!) Execution



Insert: head := elem;



Definitions

- Several important terms
 - State
 - The values of all program variables, both implicit and explicit, at a given point in time
 - Atomic action
 - an action that indivisibly examines or changes program state
 - an operation that, once started, runs to completion
 - more precisely, logically runs to completion
 - we assume loads and stores are physically atomic
 - meaning: if thread A stores "1" into variable x and thread B stores "2" into variable x at about the same time, result is either "1" or "2"

Definitions, continued

- Additional terms
 - History
 - Linearization (interleaving) of the atomic actions of all threads
 - Different histories may lead to the same output
 - Safety: program never enters a bad state
 - Example: partial correctness
 - Liveness: program eventually enters a good state
 - Example: termination

Definitions, continued

- Additional terms
 - Interference
 - Thread 1 interferes with Thread 2 if:
 - Thread 1 executes an assignment statement that modifies a shared variable that invalidates an assertion in Thread 2

Example of Interference Assertions are in {...}

- int x = 0
- CO
 - ${x == 0}$

x = x + 1

 $\{x == 1\}$

- Assignment in thread 1
 - Assertion: represents state after assignment in thread 1

Assertion: represents state before assignment in thread 1

Invalidated! $\{x == 0\}$ x = x + 2 $\{x == 2\}$

OC

Assertion: represents state before assignment in thread 2 Assignment in thread 2 Assertion: represents state after assignment in thread 2

Race Condition

- When output depends on ordering of thread execution
- More formally:
 - (1) two or more threads access a shared variable with no synchronization, and
 - (2) at least one of the threads writes to the variable

Both the addition code and the list code shown previously have race conditions

General Form of Atomic Operation (Removing Race Conditions)

- $\operatorname{await}(B)$ S> Called a *conditional atomic action*
 - Atomically do (all of) the following:
 - Evaluate B
 - Wait until B is true
 - Execute S (an arbitrary statement list)
 - If the "await (B)" is omitted, S is immediately executed, but still atomically
 - <...> hides intermediate states and reduces number of histories

Example With Await

```
int x = 0
CO
 x = x + 1
<(await x == 1) x = x + 2>
OC
print x
```

This program will always output 3. (It also serializes execution.)

Example with Atomic Operations

int x = y = 0, z

CO

 $<_{x} = 1>; <_{z} = x+y>$ // $<_{y} = 2>; <_{z} = x-y>$ oc

What are the possible final values of x, y, and z? How many histories are there?

Example with Atomic Operations

int x = y = 0, z

CO

 $<_{x} = 1>; <_{z} = x+y>$ //

 $<y = 2>; <_Z = x-y>$

OC

Vars x and y must be 1 and 2; z can be -1 or 3 Number of histories is 6

General formula: $(n^*m)! / (m!^n)$, where n is number of threads and m is number of atomic actions per thread 15

Same Example, Removing Explicit Atomicity

int x = y = 0, z
co
x = 1; z = x+y
//
y = 2; z = x-y
oc

What are the possible final values of x, y, and z?

Same Example, Removing Explicit Atomicity int x = y = 0, zCO x = 1; z = x + yy = 2; z = x-y \mathbf{OC} As before, x and y must be 1 and 2, but while z can still

As before, x and y must be 1 and 2, but while z can still be -1 or 3 (as before), it can now also be -2 or 1 Note that enumerating all histories here is impractical Via previous formula: $(10!) / (5!^2) == 252$ histories (2 threads, 5 atomic actions each)

Scheduling policies for atomic actions

- Unconditional fairness
 - Every unconditional atomic action eventually executes
 - Round robin scheduling satisfies this
- Weak fairness: UC + conditional atomic actions execute if true and seen by the thread
- Strong fairness: UC + conditional atomic actions execute if true infinitely often

Scheduling policies: WF vs. SF continue := true; try := false CO while (continue) {try := true ; try := false} <await (try) continue := false> **OC**

 With weak fairness, program may never terminate; with strong fairness, it will terminate
 Practical schedulers, however, are not strongly fair

Sequential version

```
int max = MINVAL
int a[n]
for i = 0 to n-1 {
    if (a[i] > max)
        max = a[i]
}
```

Incorrect parallel version

```
int max = MINVAL
int a[n]
co i = 0 to n-1 {
    if (a[i] > max)
        max = a[i]
}
```

Correct but slow parallel version

```
int max = MINVAL
int a[n]
co i = 0 to n-1 {
    <if (a[i] > max)
        max = a[i]>
}
```

Another incorrect parallel version

```
int max = MINVAL
int a[n]
co i = 0 to n-1 {
    if (a[i] > max)
        <max = a[i]>
}
```

Correct, efficient (but complicated) parallel version int max = MINVALint a[n] co i = 0 to n-1if $(a[i] > max) \{ \leftarrow$ Why do this? $\langle if(a[i] > max) \checkmark$ $\max = a[i] >$ 24